

Lecture 09 Code Reuse (part 2)

CTI	crunch	Computer and Network Security	
		November 25, 2019	
		Computer Science and Engineering Department	
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CNS Ò		Code Reuse	
CTF crunch			Notes
		.text in executable and in libc	
	rewrite code pointe jump to functions	ers or parts of functions	
•	ret-to-libc, ROP, J	OP, DOP	
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CNS		Return Oriented Programming	Notes
•	jump to small area	s ending in ret	<u> </u>
▶	control the stack		
	each ret pops and Turing-complete	ther code address from the stack	
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CNS Ò		Sample Gadgets	
CTF crunch		- Cumple Guagets	Notes
	pop rdi; ret — f		
		i; ret – fill first two arguments t – move stack pointer	
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Notes

ROP API

r = ROP(e)
pop_rdi_ret = r.find_gadget(["pop rdi", "ret"]).address

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CNS

Reminder: Uses

- ▶ call functions on x86_64 (populate registers)
- ▶ call multiple functions (free stack after each call on i386)

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Multi-Phase Attack

- multiple stage payloads
- return-to-main
- $\,\blacktriangleright\,$ first payload make room for the second one
- $\,\blacktriangleright\,$ first payload leaks data for the second one (ASLR bypass)

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CNS

Multi-Phase Attack: 32 bit Sample

2 bit Sample

payload_stage_1 = offset * "A" + pack(puts_plt) +
 pack(main_address) + pack(puts_got)
payload_stage_2 = offset * "A" + pack(system_address) +
 4 * "B" + pack(bin_sh_address)

Notes Notes Notes Notes

payload_stage_1 = offset * "A" + pack(pop_rdi_ret) +
 pack(puts_got) + pack(puts_plt) + pack(main_address)
payload_stage_2 = offset * "A" + pack(pop_rdi_ret) +
 pack(bin_sh_address) + pack(system_address)

CNS

Not Enough Space

- $\,\blacktriangleright\,$ buffer overflow insufficient for a ROP chain
- use two stages of payload: make first (main) stage triggers second stage
- call main again, trigger a reading function, use stack pivoting

CNS

Stack Pivoting

- ▶ move stack pointer to a new location (heap, data, higher on
- ▶ the new location stores the rest of the ROP chain
- ▶ needs leak / knowledge of address to pivot to

CNSO

Stack Pivoting Instructions

xchg rsp, rax add rsp, 32 sub rsp, 32 leave ; ret mov rsp, rax

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CNS	leave; ret Gadget	Notes
called at the end of the		
	ent so you get a new value for rbp ; ret; fill rsp with rbp address	
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CNS	Stack Pivoting Payloads	Notes
64 bit Sample first_payload = offset * "A" +	+ p64(stack_pivot_address) +	
p64(leave_ret_gadget) second_payload = 8 * "B" + p64 p64(function_address)	4(pop_rdi_ret) + p64(argument) +	
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CNSO	Stack Space Issue	Notes
	s needs to have space before that	
stack is going to increa	ase (i.e. rsp goes down) and needs space	
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CNS	Keywords	Notes
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multi-phase attackputs() leak	▶ stack pivot	
▶ GOT leak	▶ leave; ret gadget	
libc address		

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References

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